

CLAIMS

We claim:

1. An integrated circuit for a computer peripheral device, comprising:

5 an interface translator which receives subsystem control instructions from a computer, translates those instructions into an internal format that require no further processing and are understood by the subsystems of the peripheral device, and passes the instructions to the appropriate subsystem component.

10 2. The integrated circuit as in claim 1, further comprising:

internal memory communicating with the interface translator, for buffering processed data from the computer.

3. The integrated circuit as in claim 1, wherein:

15 the computer peripheral device includes the capability of printing, and the instructions control the subsystems to print an image.

4. The integrated circuit as in claim 1, wherein the computer peripheral device includes the capability of scanning.

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5. A system, comprising:

 a computer having a central processing unit (CPU);
 a peripheral device having an interconnection to the computer; and
 an interface translator on the peripheral device;

25 wherein the computer CPU generates control instructions for the subsystems of the peripheral device; and

 wherein the interface translator receives subsystem control instructions from the computer CPU, translates those instructions into an internal format that require no further processing and are understood by the subsystems of the

peripheral device, and passes the instructions to the appropriate subsystem component.

6. The system as in claim 5, wherein the peripheral device includes the capability of
5 printing, and the instructions control the subsystems to print an image.

7. The system as in claim 5, wherein the peripheral device includes the capability of
scanning.

10 8. A method for printing an image on a peripheral device for use with a host computer,
comprising:

receiving subsystem control instructions for printing an image from the host
computer;

translating the instructions into an internal format that require no further
15 processing and are understood by the subsystems of the peripheral device; and
passing the instructions to the appropriate subsystem component to print the
image.

9. The method as in claim 8, further comprising:

20 performing image processing and generating subsystem control instructions at the
host computer prior to receiving subsystem control instructions.